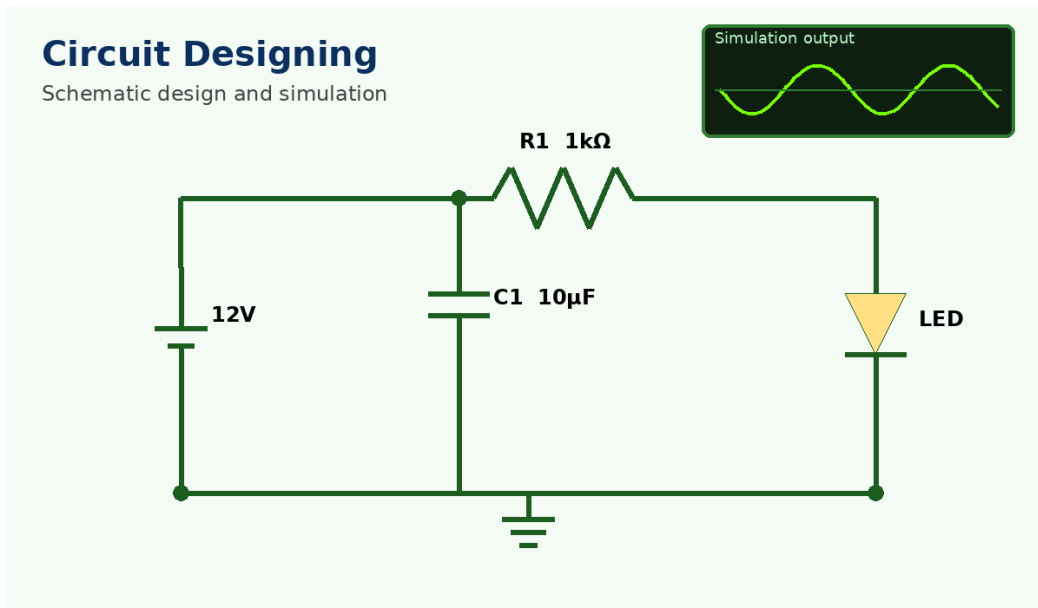


CIRCUIT DESIGNING COMPETITION

Official Rules, Event Structure, and Guidelines Document



General Regulations

1. Reporting & Attendance

All participants must be present at the venue before 9:30 AM. The first round will begin sharp at 9:30 AM, and no round will be delayed due to late arrival.

2. No Cheating & Integrity

Any form of unauthorized assistance, external help, or cheating during any stage of the competition will result in immediate disqualification.

3. Fixed Time Limits

Each round has a strict, fixed duration. No extra time will be provided once the allotted competition time has officially ended.

4. Organizer's Decision

The decision of the organizers and judges regarding evaluation, functionality validation, time-logging, and disqualification will be final and binding.

Round 1: Screening

5. Round 1 – Paper-Based MCQ Screening Written Test

A physical, written technical test covering fundamental concepts in Digital Logic Design (DLD), Electronics, and general circuit theory.

Progression: The top-performing teams will qualify to advance to Round 2.

Round 2: Simulation & Debugging

6. Round 2 – Simulation & Debugging (30 Minutes)

A 30-minute timed round where qualified teams perform schematic design or circuit debugging using simulation software.

Requirements & Resources: Each team must bring a fully charged laptop with the required simulation software installed and tested beforehand. Participants must arrange their own internet source/hotspot if needed.

Progression: The top-performing teams from this round will advance to the Grand Finale, strictly capped at a maximum of 5 finalist teams.

Round 3: Grand Finale & Prizes

7. Round 3 – Hardware Implementation / Grand Finale (30 Minutes)

The final pool of top teams will compete in a physical hardware building and testing challenge on breadboards/components.

Winning Condition: The first team to successfully build, test, and demonstrate a fully working, error-free circuit within the 30-minute limit will be declared the Winner.

Accuracy Rule: A rapid submission that fails functionality checks will incur penalties or be ranked behind a correctly functioning circuit.

8. Podium & Awards

The competition will officially award One Winner and Two Runners-Up based on performance and completion speed in the final hardware round.

Good luck to all participating teams! May the best engineers win.